

**KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE**

(An Autonomous Institute under Kakatiya University, Warangal)

Opp : Yerraguttugutta, Sheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA.

కాకతీయ ప్రాధ్యోగికీ एवं विज्ञान संस्थान, వరంగల్ - 506 015 తెలంగాణ రాష్ట్రం, భారత్

కాకతీయ విశ్వవిద్యాలయం, ఆదర్శ - 506 015 తెలంగాణ రాష్ట్రం, భారత్

Office of the Controller of Examinations

for Regular**2022 Admitted Batch**

No. URR18_R22- B.Tech. III-Sem./ESE Regular Schedule /2023

Date: 10-11-2023

REVISED END SEMESTER EXAMINATIONS (ESE) OF URR18_R22 B.TECH. III-SEMESTER REGULAR EXAMINATIONS, November/December-2023

Ref: Circular No: KITS/Acad/ Circular/ UG-2023/221, Dated 28.11.2023.

REVISED IME - TABLE**Examination Timings: FN:: 10.00 am to 01.00 pm**

Date & Day of Examination	CE	ME	CSE	EEE	ECE	IT	CSN	ECI	CSO(IoT)	CSM(AI ML)
29.11.2023 Wednesday	Engineering Mathematics-III	Engineering Mathematics - III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III
04.12.2023 Monday	Fluid Mechanics	Material Science and Metallurgy	Discrete Mathematics	Electromagnetic Fields	Signals & Systems	Discrete Mathematics	—	Electronic Measurements and Sensors	Fundamentals of Internet of Things	Formal Languages and Automata Theory
06.12.2023 Wednesday	Surveying	Engineering Thermodynamics	Computer Architecture and Organization	Network Theory	Network Analysis	Computer Architecture and Organization	Computer Architecture and Organization	Signals Systems and Random Processes	—	Computer Organization and Architecture
08.12.2023 Friday	Construction Materials	Machine Drawing	Advanced Data Structures	Electronic Devices and Circuits	Analog Circuits - I	Advanced Data Structures	Advanced Data Structures	Electronic Devices and Applications	Advanced Data Structures	Advanced Data Structures
12.12.2023 Tuesday	—	—	Digital Electronics	—	Switching Theory & Logic Design	Switching Theory and Logic Design	Digital Electronics	Digital Circuits and Logic Design	—	—
14.12.2023 Thursday	—	—	—	—	—	Operating Systems	Operating Systems	—	Computer Networks	Operating Systems
15.12.2023 Friday	—	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge (only for Lateral Entry students)	—	—	Essence of Indian Traditional Knowledge (only for Lateral Entry students)	Essence of Indian Traditional Knowledge (only for Lateral Entry students)	—	Essence of Indian Traditional Knowledge (only for Lateral Entry students)	—
Examination Timings: 02:00 pm to 05:00 pm										
16.12.2023 Saturday Revised Date	OE-I Strength of Materials	Mechanics of Solids	Object Oriented Programming through JAVA	Electrical Measurements & Instrumentation	OE-I Object Oriented Programming	—	Object Oriented Programming through JAVA	OE-I Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA

Candidate should occupy his/her seat atleast five minutes before the commencement of the examination

To: All Notice Boards/ Class WhatsApp groups

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3. Dean, Academic Affairs

4. Prof. I/C. Training & Placement

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